



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Takaaki Nagai

Serial No.: 09/837,734

Filed: 04/17/2001

NON-VOLATILE SEMICONDUCTOR STORAGE APPARATUS WITH
OVERLAPPING GATES AND MANUFACTURING METHOD THEREOF

ART UNIT: 2811

SUBMISSION OF CORRECTED DRAWINGS

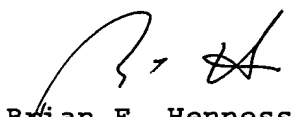
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

SIR:

Enclosed please find replacements sheets for Figures 4
and 5 in the above referenced application.

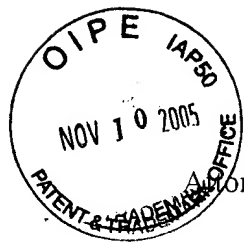
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Respectfully submitted,


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Docket No.: NECF 18.591 (100806-17366)
Ach: fdraw

Filed Express Mail
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ON 11/10/05
Pursuant to 37 C.F.R. 1.10.
by



Attorney Docket No.: NEC 18.591 (100806-17366)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : **Takaaki Nagai**
Title : **NON-VOLATILE SEMICONDUCTOR STORAGE ...**
Serial No. : **09/837,734**
Confirmation No. : **9062**
Filed : **04/17/2001**
Examiner : **Douglas W. Owens**
Group Art Unit : **2811**

Mail Stop: Issue Fee
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Comments on Statement of Reasons for Allowance

Sir:

The referenced application was allowed on August 19, 2005. The issue fee due on November 21, 2005 is being paid concurrently herewith.

Applicant hereby acknowledges the Examiner's Reasons for Allowance. Applicant respectfully notes that there may be additional reasons for allowance that have not been specifically cited, and which may apply to various of the allowed claims, in addition to or instead

To the extent that any statements regarding patentability of any claims allowed by the Examiner made by the Applicant or the Examiner in any document filed in this application are inconsistent with or not included in the Examiner's Reasons for Allowance, they are incorporated by reference herein.

Respectfully submitted,

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